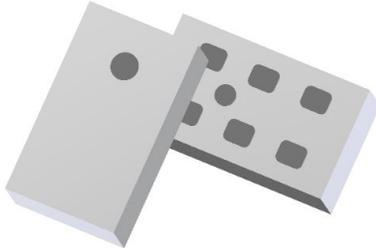




**Ultra Low Profile 0603 50Ω Power Divider**



**Description:**

The PD2635L5050S2HF2 is a low profile, sub-miniature Wilkinson power divider in an easy to use Xinger style manufacturing friendly surface mount package. The PD2635L5050S2HF2 is ideal for high volume manufacturing and delivers higher flexibility than traditional printed and lumped element solutions. The PD2635L5050S2HF2 is matched to 50 Ω and has a height profile of 0.64mm, which is ideal for high-level integrations in the following markets: 5G, LTE & S-Band Mil-Aero. The PD2635L5050S2HF2 does not include the resistive element and therefore, requires an external resistor (also available from TTM) for operation. The PD2635L5050S2HF2 is available on tape and reel for high volume manufacturing pick and place.

**Features:**

- 2496-4000 MHz
- 5G and S-Band
- 0.64mm Height
- 50Ω Input
- 50Ω Output
- Low Insertion Loss
- Surface Mountable
- Tape & Reel
- Isolated Surface
- RoHS Compliant
- Halogen Free

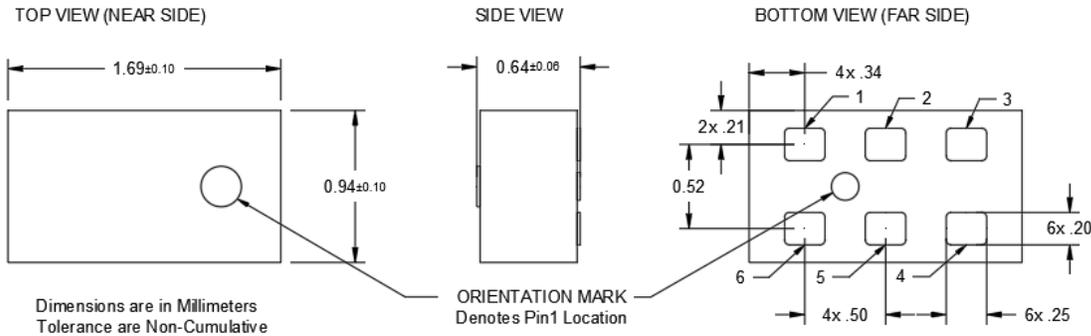
**Detailed Electrical Specifications:**

Parameter (25°C)	Min	Typ	Max	Unit									
Frequency	2496		2600	2600		3500	3400		3800	3800		4000	MHz
Input Port Impedance		50			50			50			50		Ω
Output Port Impedance		50			50			50			50		Ω
Return Loss	13	16		14	17		14	17		12	17		dB
Insertion Loss		0.4	0.5		0.3	0.5		0.4	0.6		0.4	0.7	dB
Amplitude Balance		0	0.3		0	0.3		0	0.3		0	0.4	dB
Phase Balance		0.2	3		0.2	3		0.2	4		0.2	4	Degrees
Isolation (Output Ports)	16	18		15	20		15	20		14	20		dB
Power Handling (105°C)			2			2			2				Watts
Operating Temperature	-55		140	-55		140	-55		140	-55		140	°C

\* Insertion Loss stated at room temperature (Insertion Loss is approximately 0.1 dB or less higher at +105 °C)

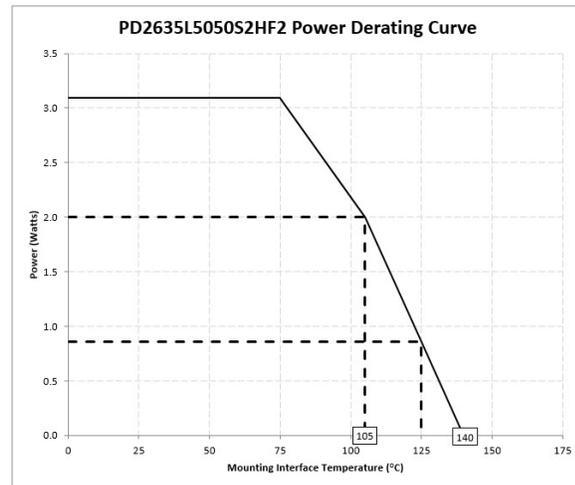
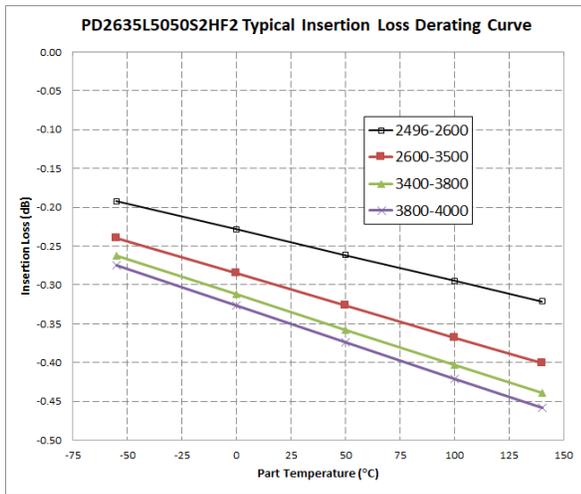
\*\* Specifications subject to change without notice

**Outline Drawing:**



Pin	Designation
1	Ground
2	Input
3	Ground
4	Output 1
5	Ground
6	Output 2

## Insertion Loss and Power Derating Curves



### Insertion Loss Derating:

The insertion loss, at a given frequency, of a group of Power Divider is measured at 25°C and then averaged. The measurements are performed under small signal conditions (i.e. using a Vector Network Analyzer). The process is repeated at -55°C, 105°C, and 140°C. A best-fit line for the measured data is computed and then plotted from -55°C to 140°C.

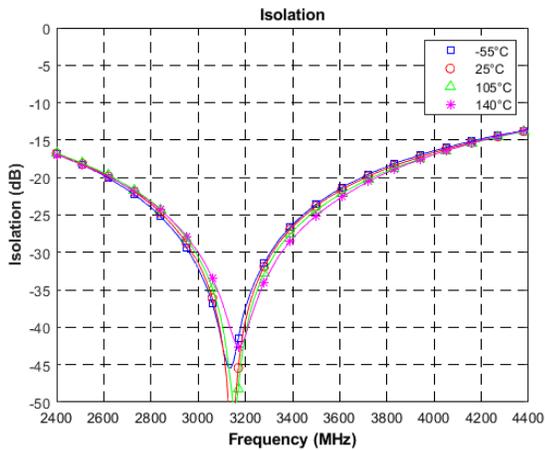
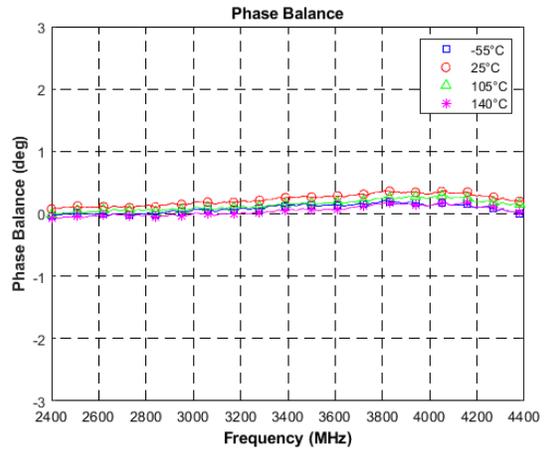
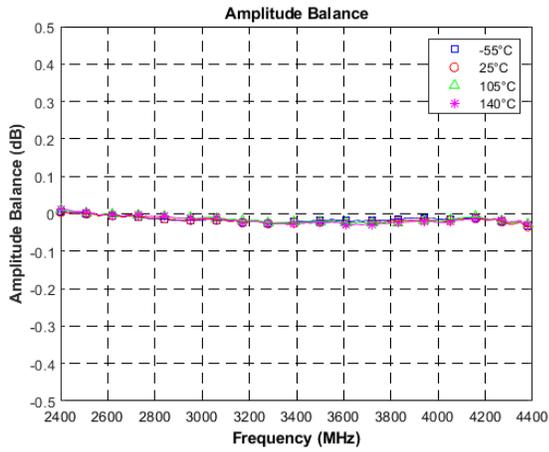
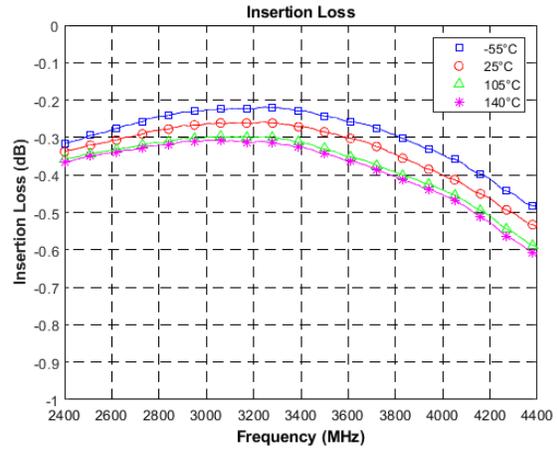
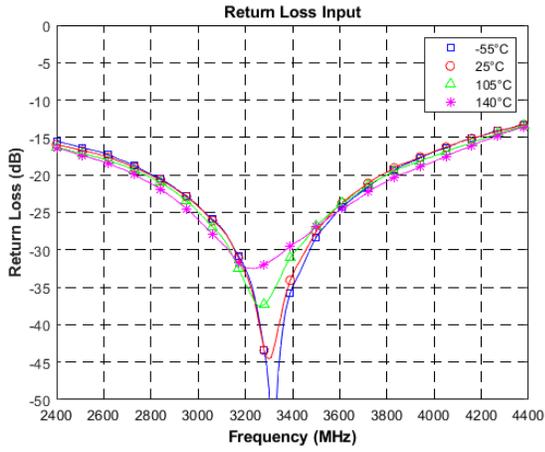
### Power Derating:

The power handling and corresponding power derating plots are a function of the thermal resistance, mounting surface temperature (base plate temperature), maximum continuous operating temperature of the Power Divider, and the thermal insertion loss. The thermal insertion loss is defined in the Power Handling section of the data sheet.

As the mounting interface temperature approaches the maximum continuous operating temperature, the power handling decreases to zero.

If mounting temperature is greater than 105°C, Power Divider will perform reliably as long as the input power is derated to the curve above.

Typical Performance: 2400 MHz to 4400 MHz



## Definition of Measured Specifications

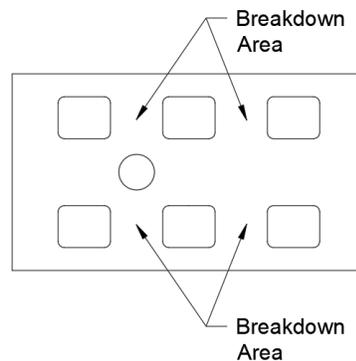
Parameter	Definition	Mathematical Representation
<b>Insertion Loss</b>	The measure of the sum of the power at outputs reference to the input power	$IL(dB) = 10Log_{10}( S_{21} ^2 +  S_{31} ^2)$
<b>Return Loss</b>	The measure of the power of the signal reflected by discontinuity in a transmission line relative to incident power	$RL_{IN}(dB) = 20Log_{10}(S_{11})$ $RL_{out1}(dB) = 20Log_{10}(S_{22})$ $RL_{out2}(dB) = 20Log_{10}(S_{33})$
<b>Isolation</b>	The measure of the power of the signal reflected by discontinuity in a transmission line relative to incident power	$ISO_{23}(dB) = 20Log_{10}(S_{23})$
<b>Amplitude Balance</b>	The measure of the difference in power levels between each of the two outputs	$ABal_{23}(dB) = 20Log_{10} S_{21}/S_{31} $
<b>Phase Balance</b>	The measure of the difference in phase between each of the two outputs.	$PBal_{23}(degree) = ang(S_{21}/S_{31})$

\*Parts are 100% RF tested as per spec definition.

\*\* Refer to page 1 for port assignment

## Peak Power Handling:

High-Pot testing of these components during the qualification procedure resulted in a minimum breakdown voltage of 1Kv (minimum recorded value). This voltage level corresponds to a breakdown resistance capable of handling at least 12dB peaks over average power levels, for very short durations. The breakdown location consistently occurred across the pads and the ground bar (see illustration below). The breakdown levels at these points will be affected by any contamination in the gap area around these pads. These areas must be kept clean for optimum performance. It is recommended that the user test for voltage breakdown under the maximum operating conditions and over worst case modulation induced power peaking. This evaluation should also include extreme environmental conditions (such as high humidity).



**Packaging and Ordering Information:**

Parts are available in reel and are packaged per EIA 481. Parts are oriented in tape and reel as shown below. Minimum order quantities are 4000 per reel.

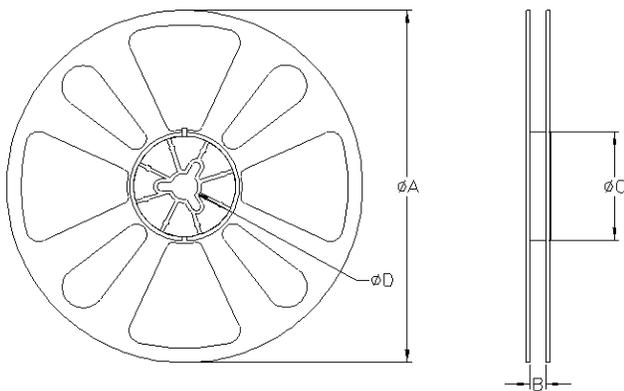
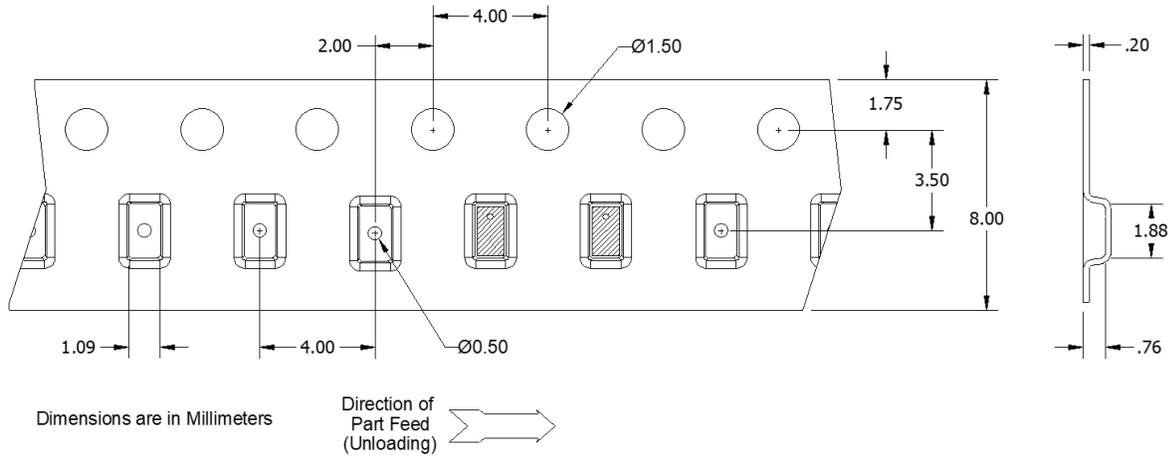


TABLE 1		
QUANTITY/REEL	REEL DIMENSIONS mm	
4000	ØA	177.80
	B	8.00
	ØC	50.80
	ØD	13.00

**Contact us:**  
[rf&s\\_support@ttm.com](mailto:rf&s_support@ttm.com)